

					PTO/SB/21 (08-00)		
TRANSMITTAL			Application Number		09/916,484		
			Filing Date		July 30, 2001		
FORM		First Named Inventor		Shunpei YAMAZAKI et at.			
			Group Art Unit		2823		
(to be used for all correspondence after initial filing)		Examiner Name		S. Foong			
Total Number of Pages in This Submission			Attorney Docket Number		0756-2344		
ENCLOSURES (check all that apply)							
Fee Transmittal Form  Fee Attached  Amendment / Reply  After Final  Affidavits/declaration(s)  Extension of Time Request  Express Abandonment Request  Information Disclosure Statement  Certified Copy of Priority Document(s)  Response to Missing Parts/		(for an and Drawing Declara Attorne Licensing Petition Provision Power of Change Address Termina Request CD, Nu	ation and Power of y ng-related Papers to Convert to a onal Application of Attorney, Revocation e of Correspondence s al Disclaimer st for Refund mber of CD(s)		After Allowance Communication to Group  Appeal Communication to Board of Appeals and Interferences  Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)  Proprietary Information  Status Letter  Other Enclosures  1  2  3  4  5  6  v authorized to charge any additional ayments to Deposit Account No. 50-ket number.		
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT							
Eric J. Robinson, Reg. No. 38,285 Robinson Intellectual Property Law Office, P.C. PMB 955 ndividual name 21010 Southbank Street Potomac Falls, VA 20165							
Signature	5						
Date	January 24, 2003						
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Docket No. 0756-2344

INITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of	)	Art Unit: 2823
Shunpei YAMAZAKI et al.	)	Examiner: S. Foong
Serial No. 09/916,484	)	CERTIFICATE
Filed: July 30, 2001	)	I hereby certify that this corresp The United States Postal Service Class Mail in an envelope addres Washington, D.C. 20231, on Janu
For: ELECTRIC DEVICE, MATRIX	)	
DEVICE, ELECTRO-OPTICAL DISPLA	YY)	
DEVICE, AND SEMICONDUCTOR	)	Rose M. Fichtel
MEMORY HAVING THIN-FILM	)	
TRANSISTORS	)	

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## **AMENDMENT**

Honorable Commissioner of Patents Washington, D.C. 20231

Sir:

In response to the Office Action dated October 24, 2002, please amend the above-identified application as follows:

## **IN THE CLAIMS:**

Please amend claims 3, 9, and 15 as follows:

3. (Twice Amended) A method of manufacturing a semiconductor device: the semiconductor device comprising: at least two p-channel thin film transistors in a pixel portion,

each of the two p-channel thin film transistors in the pixel portion fabricated through the method comprising:

forming a semiconductor island over a substrate;

forming a gate electrode adjacent to the semiconductor island with a gate insulating film therebetween;